

10824713_CLS1.txt
Most Frequently Occurring Classifications of Patents Returned
From A Search of 10824713 on June 08, 2006

Original Classifications

6 710/22
5 365/230.05
4 710/27
3 365/189.05
3 710/28
3 710/315
3 711/118
2 358/1.15
2 365/185.14
2 365/185.17
2 710/100
2 710/107
2 710/23
2 710/240
2 710/25
2 710/305
2 710/307
2 711/135
2 711/137
2 711/141
2 711/146
2 711/147
2 712/36

Cross-Reference Classifications

- 9 710/22
- 5 710/52
5 711/154
4 257/E27.103
4 365/230.08
4 711/100
3 365/189.04
3 365/201
3 365/218
3 365/230.03
3 365/231
3 710/23
3 710/26
3 710/27
3 710/39
3 711/141
3 711/143
3 711/146
3 711/150
3 711/202
3 711/206
2 257/E27.016
2 257/E27.101
2 345/501
2 345/520
2 345/536
2 358/1.1
2 365/185.06
2 365/185.11
2 365/185.17
2 365/185.29
2 365/189.05
2 365/189.08

710/22
33
308

710/22-28
33-35
711/208
710/308 - 310

2 365/194
 2 365/207
 2 365/221
 2 365/227
 2 365/230.01
 2 365/230.05
 2 365/233
 2 710/10
 2 710/100
 2 710/24
 2 710/28
 2 710/3
 2 710/308
 2 710/5
 2 711/127
 2 711/131
 2 711/133
 2 711/137
 2 711/142
 2 711/144
 2 711/167
 2 711/169
 2 711/201
 2 712/33
 2 712/35
 2 712/38

Combined Classifications

15 710/22
 7 365/230.05
 7 710/27
 5 365/189.05
 5 710/23
 5 710/28
 5 710/52
 5 711/141
 5 711/146
 5 711/154
 4 257/E27.103
 4 365/185.17
 4 365/189.04
 4 365/201
 4 365/218
 4 365/230.08
 4 710/100
 4 711/100
 4 711/118
 4 711/137
 4 711/143
 4 711/206
 3 365/185.14
 3 365/194
 3 365/227
 3 365/230.01
 3 365/230.03
 3 365/231
 3 710/107
 3 710/24
 3 710/240
 3 710/25
 3 710/26
 3 710/3
 3 710/305

10824713_CLS1.txt

3 710/308
3 710/315
3 710/39
3 711/147
3 711/150
3 711/167
3 711/202
3 712/36
2 257/E27.016
2 257/E27.101
2 345/501
2 345/502
2 345/520
2 345/536
2 358/1.1
2 358/1.15
2 365/185.06
2 365/185.11
2 365/185.29
2 365/185.33
2 365/189.08
2 365/200
2 365/205
2 365/207
2 365/221
2 365/233
2 365/63
2 710/10
2 710/110
2 710/29
2 710/307
2 710/31
2 710/5
2 710/53
2 711/108
2 711/113
2 711/127
2 711/131
2 711/133
2 711/135
2 711/139
2 711/142
2 711/144
2 711/169
2 711/170
2 711/201
2 712/225
2 712/33
2 712/34
2 712/35
2 712/38
2 714/764

EAST - [Untitled (Recovered):1]

File View Edit Tools Window Help

Search List Process Queue Clear

DBs US-PGPUB:USPAT;USOCR;EPO;JPO;DERWENT;IBM_TDB

Default operator: OR

☒ Plurals

☐ Highlight all hit terms initially

Saved

- S5: (2762) dma near9 engine
- S6: (341) S4 with S5
- S3: (1144) dma near9 descriptor
- S14: (2762) dma near9 engines
- S4: (3671) (multiple plurality respective) near9 dma
- S7: (27) S3 with S6
- S8: (122) S3 with S4
- S2: (85) dma and S1
- S19: (41) S18 near9 descriptor
- S18: (723) dma near9 engines
- S17: (39) next with S15
- S16: (0) streaming with S15
- S13: (1) ("5864712").PN.
- S12: (1) ("5864713").PN.
- S1: (149) ("5465261") or ("5671445") or ("4987595") or ("54405")
- S15: (297) S14 near9 descriptor
- S11: (64) S10 not S9
- S10: (79) chain near9 S3
- S9: (95) S8 not S7
- S26: (56) communication with S24
- S23: (61) descriptor with S22
- S22: (571) dma with mac
- S20: (21) communication near9 sequence near9 dma
- S21: (48) dma with descriptor\$2 with engines
- S25: (9) cdmac
- S24: (606) dma near2 engines
- S27: (46) descriptor with S24

BRS form S&R form Image Text HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	3	Im
1	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6735642 B2	20040511	11	DMA doorbell	710/24	710/22;		Kagan; Michael et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6678749 B2	20040113	16	System and method for efficien	710/5	710/18;		Kolli; Praveen K. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 5644784 A	19970701	13	Linear list based DMA control str	710/24	710/35		Peek; Gregory A.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 20050138233	20050623	10	Direct memory access control	710/24			Bibikar, Vasu J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 20050044283	20050224	20	Apparatus having an inter-mod	710/8			Muro, Tomoharu	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
6	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 20050033874	20050210	11	Direct memory access using me	710/22			Futrel, William T. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
7	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6874054 B2	20050329	16	Direct memory access controlle	710/310	710/22;		Clayton; Shawn Adam e	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
8	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6868458 B2	20050315	13	Communication system for rabi	710/28	710/23;		Kim; Jung-hyun	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
9	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6782465 B1	20040824	6	Linked list DMA descriptor archit	711/208	710/22;		Schmidt; Andreas	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
10	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6954806 B2	20051011	30	Data transfer apparatus and m	710/22	710/23;		Yosimoto; Atuyuki et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

Hits Details HTML

Ready

NUM

BEST AVAILABLE COPY

BEST AVAILABLE COPY

- BRS:
- ☐ Pending
 - ☒ Active
 - ☒ L1: (149) ("5465261" or ("5671445" or ("4987595" or ("54405
 - ☒ L2: (85) dma and 1
 - ☒ L3: (1144) dma near9 descriptor
 - ☒ L4: (3671) (multiple plurality respective) near9 dma
 - ☒ L5: (2762) dma near9 engine
 - ☒ L6: (341) 4 with 5
 - ☒ L7: (27) 3 with 6
 - ☒ L8: (122) 3 with 4
 - ☒ L9: (95) 8 not 7
 - ☒ L10: (79) chain near9 3
 - ☒ L11: (64) 10 not 9
 - ☒ L12: (1) ("5864713").PN.
 - ☒ L13: (1) ("5864712").PN.
 - ☒ L14: (2762) dma near9 engines
 - ☒ L15: (297) 14 near9 descriptor
 - ☒ L16: (0) streaming with 15
 - ☒ L17: (39) next with 15
 - ☒ L18: (723) dma near9 engines
 - ☒ L19: (41) 18 near9 descriptor
 - ☐ Failed
 - ☒ Saved
 - ☒ Favorites
 - ☒ Tagged (10)
 - ☐ HSC

Search: List Remove Update Open

DBs: US-PGPUB:USPAT:USOCR:EPO:JPO:DERWENT:IBM_TDB

Default operator:

OR

☒ Bursals☐ Highlight all hit terms initially

BRS form SAR form Image Text HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	3	Im
1	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6735642 B2	20040511	11	DMA doorbell	710/24	710/22;		Kagan; Michael et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6782465 B1	20040824	6	Linked list DMA descriptor archit	711/208	710/22;		Schmidt; Andreas	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 20050044283	20050224	20	Apparatus having an inter-mod	710/8			Muro; Tomoharu	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6874054 B2	20050329	16	Direct memory access controlle	710/310	710/22;		Clayton; Shawn Adam e	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6868458 B2	20050315	13	Communication system for rasi	710/28	710/23;		Kim; Jung-hyun	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
6	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 20050033874	20050210	11	Direct memory access using me	710/22			Futrel, William T. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
7	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6954806 B2	20051011	30	Data transfer apparatus and m	710/22	710/23;		Yosimoto; Atsuyuki et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
8	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 20050138233	20050623	10	Direct memory access control	710/24			Bibikar, Vasu J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
9	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 5644784 A	19970701	13	Linear list based DMA control str	710/24	710/35		Peek; Gregory A.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
10	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6678749 B2	20040113	16	System and method for efficien	710/5	710/18;		Kolli; Praveen K. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

Hits Details HTML

Ready

NUM

BEST AVAILABLE COPY